

Preliminary Amendment

Applicant: Recai Sezi et al.

Serial No.: Unknown

(Priority Application No. DE 103 55 561.7)
(International Application No. PCT/DE2004/002601)

Filed: Herewith

(Priority Date: 28 November 2003)
(International Filing Date: 24 November 204)

Docket No.: I433.232.101/14.019

Title: SEMICONDUCTOR ARRANGEMENT WITH NON-VOLATILE MEMORIES

IN THE ABSTRACT

Please replace the Abstract with the following rewritten paragraph:

SEMICONDUCTOR ARRANGEMENT WITH NONVOLATILE MEMORIES

Abstract

Semiconductor arrangement having nonvolatile memories

One aspect of the The invention relates to a semiconductor arrangement having at least one nonvolatile memory cell which has a first electrode comprising at least two layers; and having an organic material, the organic material forming a compound with that layer of the first electrode which is in direct contact. ~~One aspect of the~~ invention furthermore relates to a method for producing the nonvolatile memory cell, a semiconductor arrangement having a plurality of memory cells according to the invention, and a method for producing the same.